

# G2K30Z0\_V1.1

CONTENT	SHEET
COVER SHEET	1
BLOCK DIAGRAM	2
SYSTEM RESOURCE	3
CPU CFG/ACPI/IO	4
CPU DDR	5
CPU RGMII/PCIE/SE/DISP	6
CPU POWER	7
DDR4 SODIMM	8
EMMC/CLOCK	9
LAN0/1	10
USB/SATA	11
eDP/BACKLIGHT/TP	12
DP/HDMI	13
ES8388	14
4G(MINIPCIE)/NVME	15
UART/IIC	16
TF/SE/WIFI	17
KEY/BEEP/RTC/FAN/WD/LED	18
DCIN/PG/AUTO/ERTC	19
S5 ACPI	20
S3 RSM	21
S0 SOC	22
S0 GPU/NODE/SOC	23
REVISION HISTORY	24

**CPU:**  
**LOONGSON 2K3000**

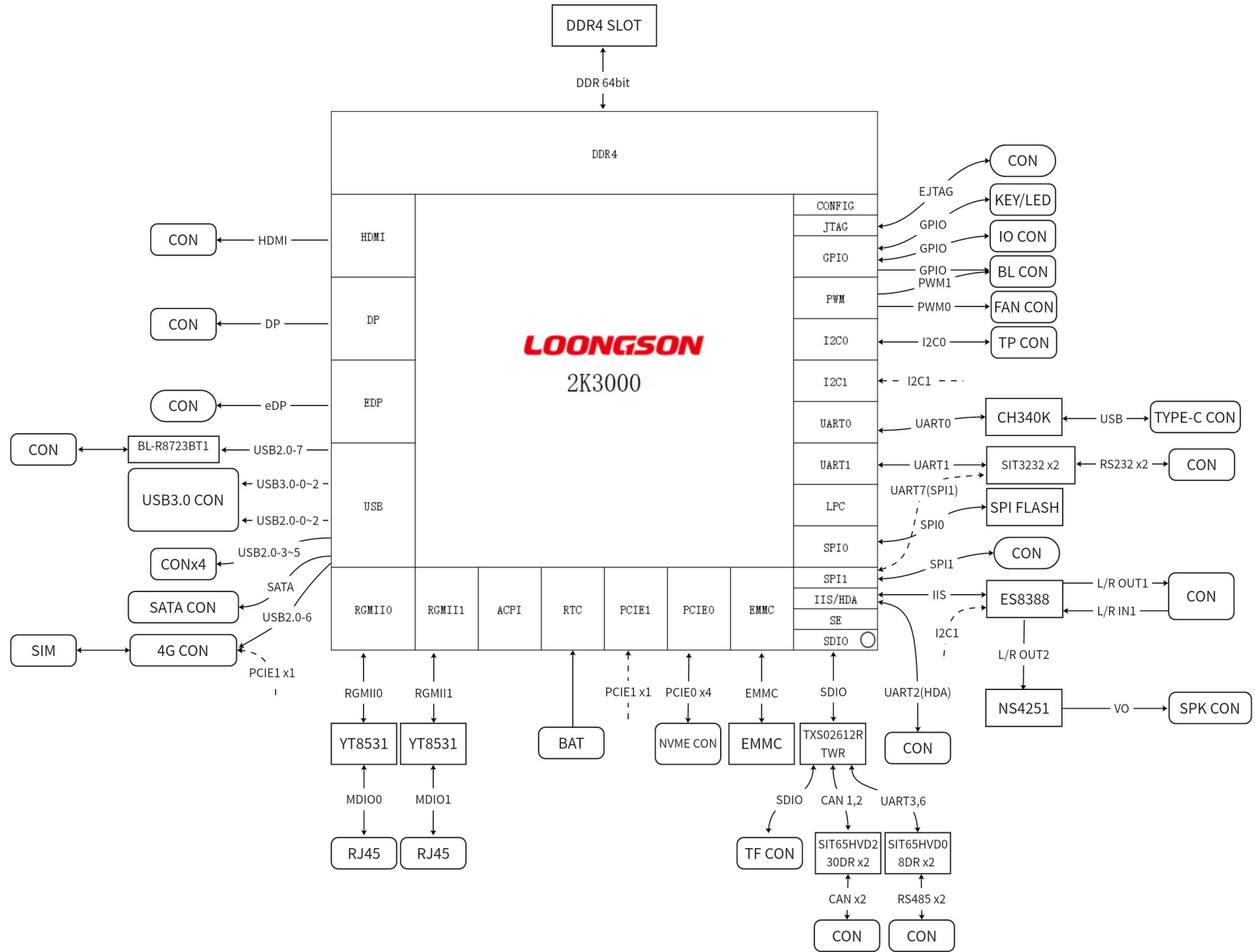
**MEM:**  
**DDR4 SODIMM**  
**EMMC 32GB**  
**NVME SSD**

**Other:**  
**MINI-PCIE SLOT**  
**SATA SLOT**  
**LAN \*2**  
**DP**  
**HDMI**  
**EDP**  
**USB3.0&USB2.0**  
**TF SLOT**

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LOONGSON TECHNOLOGY 北京市海淀区稻香湖路中关村环保科技示范园龙芯产业园	
板卡名称 G2K30Z0_V1.1	
Size C	部门 广东龙芯
Date: Wednesday, June 11, 2025	Sheet 1 of 24
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# BLOCK DIAGRAM



# SYSTEM RESOURCE

## GPIO CONFIG

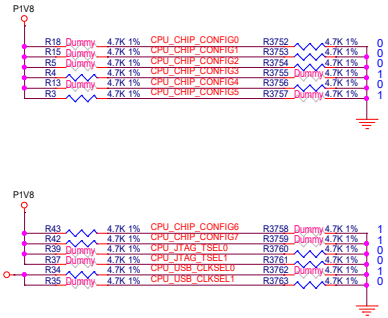
PIN NAME	REUSE	I/O	NOTE
LPC_LAD0	GPIO56	O	TF card multiplexing function switching
LPC_LAD1	GPIO57	I	User-defined buttons
LPC_LAD2	GPIO58	O	mini PCIE reset function
LPC_LAD3	GPIO59	O	FRONT LINEOUT1 sw
LPC_CLKOUT	GPIO60	O	Beep control
LPC_FRAMEn	GPIO61	O	TP reset
LPC_RESETh	GPIO62	O	NS4251 shutdown
LPC_SIRQ	GPIO63	O	Infrared detection input
PWM0	GPIO50	O	FAN control
PWM1	GPIO51	O	BACKLIGHT en
PWM2	GPIO52	O	BACKLIGHT adj
GPIO0	GPIO0	I	Headset insertion detection
GPIO1	GPIO1	O	Watch dog input
GPIO2	GPIO2	I	TP Interrupt in
GPIO3	GPIO3	O	Watch dog set
CPU_ACPI_GPIO0	ACPI_GPIO0	O	NC
CPU_ACPI_GPIO1	ACPI_GPIO1	O	NC
CPU_ACPI_GPIO2	ACPI_GPIO2	I	BOOT KEY
CPU_ACPI_GPIO3	ACPI_GPIO3	O	CAN-FD EN

## IIC CONFIG

IIC	FUNCTIONAL	Address(7-BIT)
I2C0	GPU voltage regulation control	0x2F
	NODE voltage regulation control	0x0F
	EEPROM	0x54
	DDR4 DIMM SPD ROM	0x50
I2C1	ES8388	0x10
	RTC	0x32
	I2C expansion	TBD

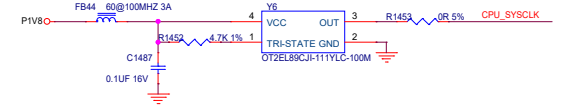
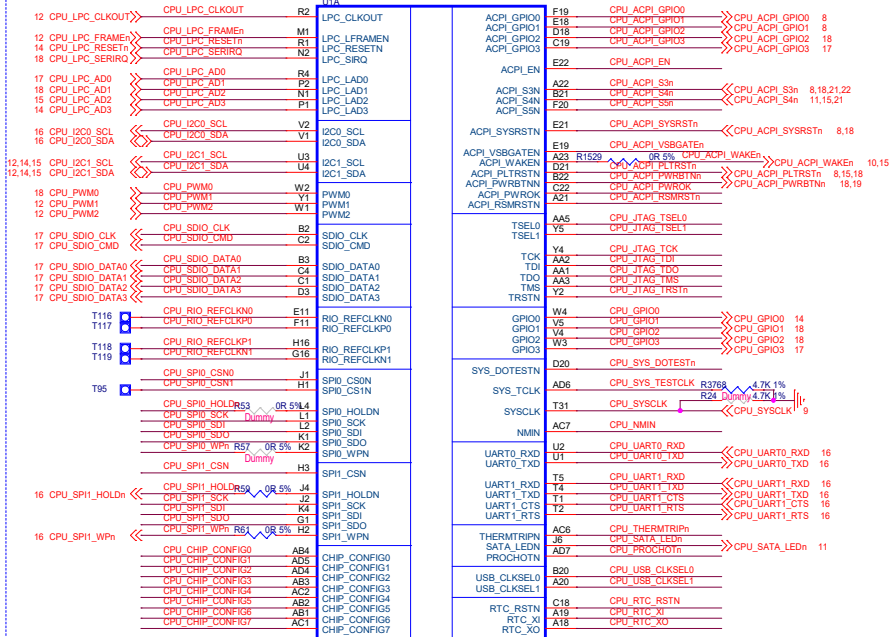
 <b>龙芯中科</b> 龙芯中科股份有限公司 <small>LOONGSON TECHNOLOGY 北京市海淀区稻香湖路中关村环保科技示范园龙芯产业园</small>	
板卡名称 G2K30Z0_V1.1	
Size C	部门 广东龙芯 Rev 1.1
Date: Wednesday, June 11, 2025	Sheet 3 of 24
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# CFG/ACPI/I/O/MISC

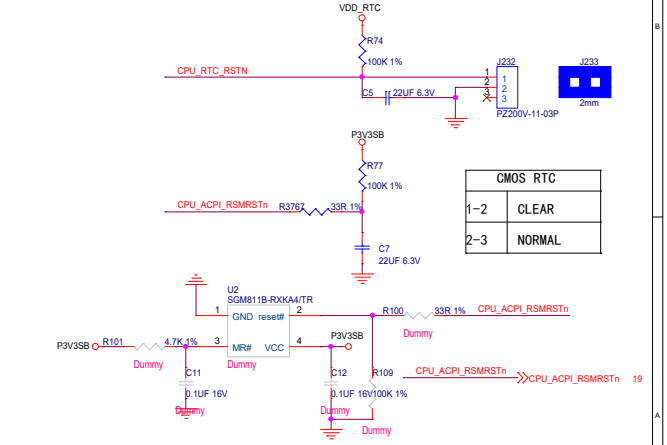
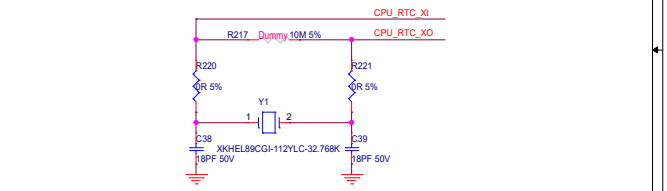
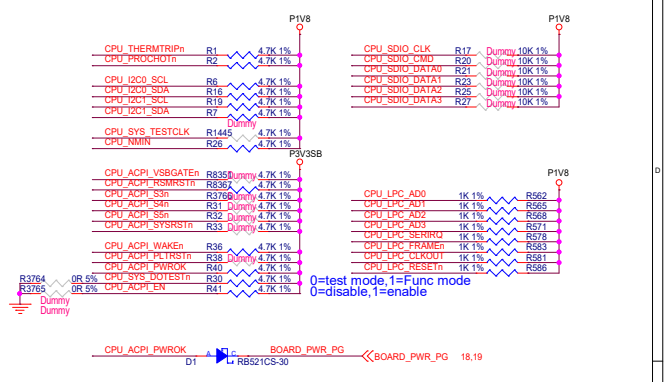
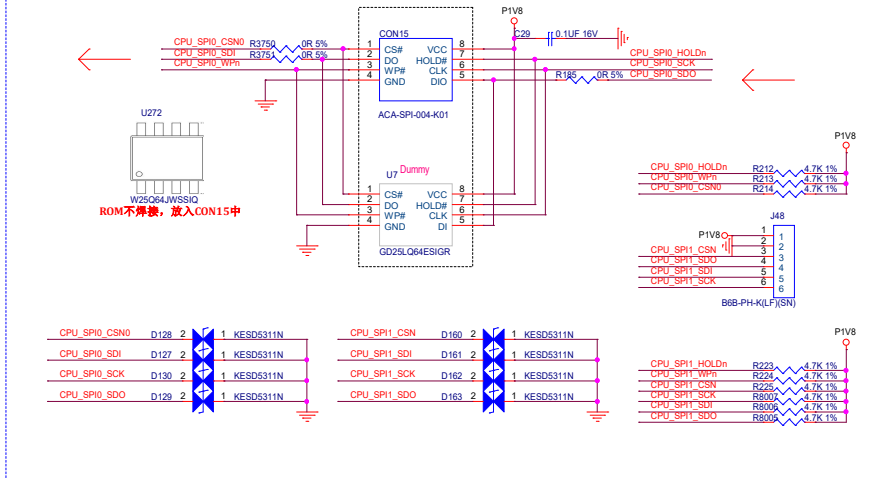
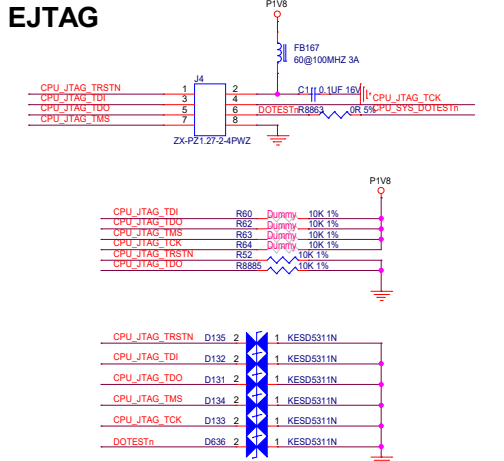


## CPU CONFIG

PIN NAME	INTERPU/PD	CONFIG
CHIP_CONFIG0	PD	系统时钟选择输入: <b>0=SYS_CLKIN</b> , <b>1=PHY</b> 输出时钟
CHIP_CONFIG1	PD	PRG 参考时钟选择: <b>0=选择 USB3 输出的25MHz 参考时钟</b> <b>1=选择 PCIE_REFCLKp/n 作为参考时钟</b>
CHIP_CONFIG2	PU	clk_sel0: 1, <b>0</b>
CHIP_CONFIG3	PD	clk_sel1: <b>1</b> , <b>0</b>
CHIP_CONFIG4	PD	SE enable: <b>1=enable</b> , <b>0=disable</b>
CHIP_CONFIG5	PD	GPU enable: <b>1=enable</b> , <b>0=disable</b>
CHIP_CONFIG6	PD	保留
CHIP_CONFIG7	PD	保留
TSEL0	PD	JTAG 模式选择 <b>0: 1, 0</b>
TSEL1	PD	JTAG 模式选择 <b>1: 1, 0</b>
USB_CLKSEL0	PU	USB时钟选择: <b>00=25MHz 晶体, 驱动能力 0;</b> <b>01=25MHz 晶体, 驱动能力 1; 单端时钟</b> <b>10=25MHz 单端输入; 11=25MHz 差分输入</b>
USB_CLKSEL1	PU	



## EJTAG



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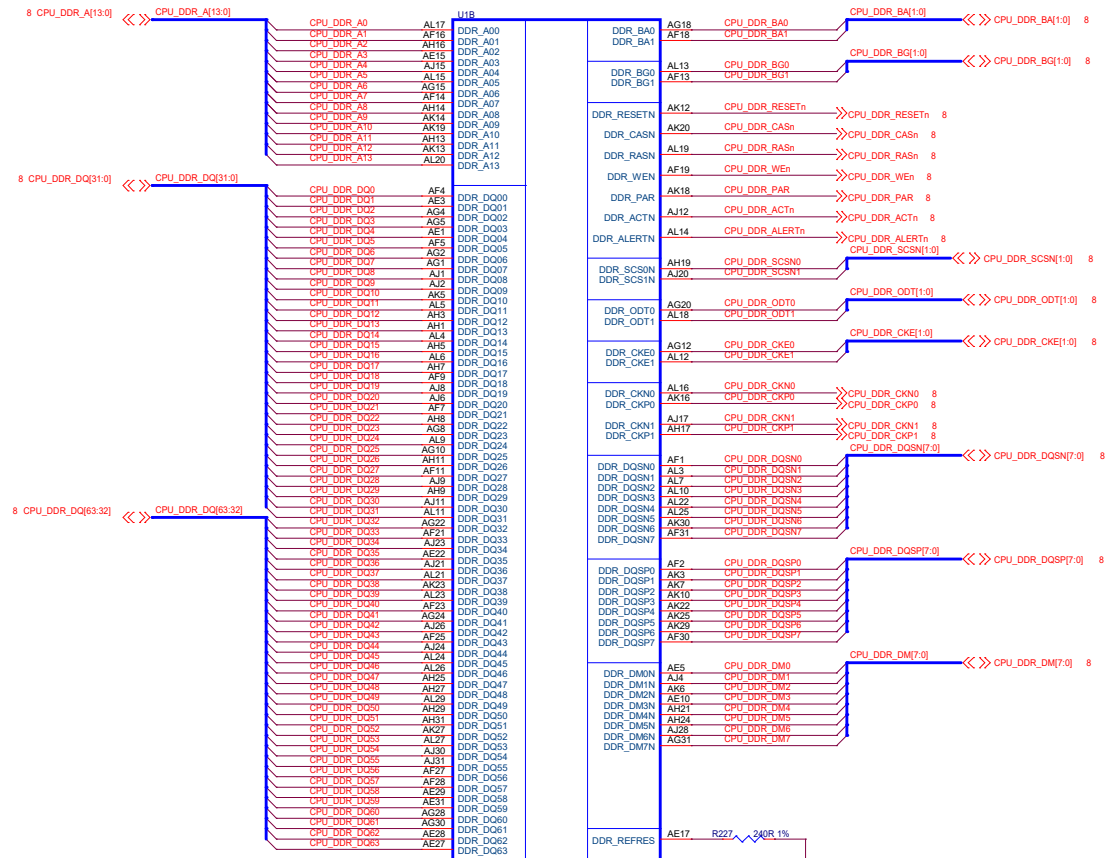
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Size: C 部门: 广东龙芯 Rev: 1.1

Date: Wednesday, June 11, 2025 Sheet 4 of 24

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DDR

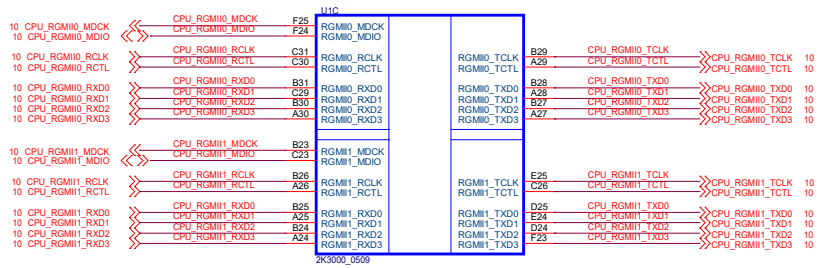


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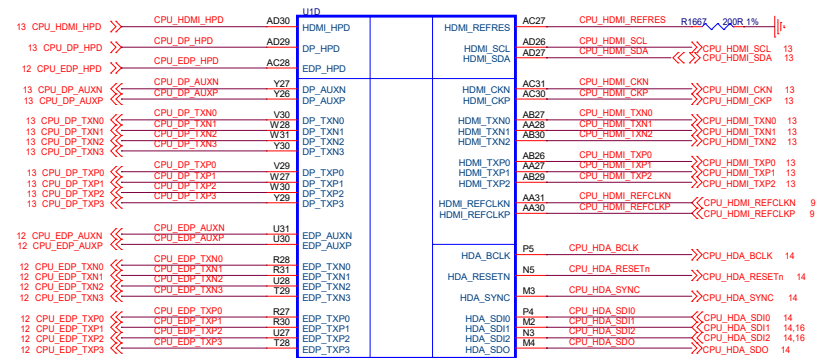
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# RGMII0/1



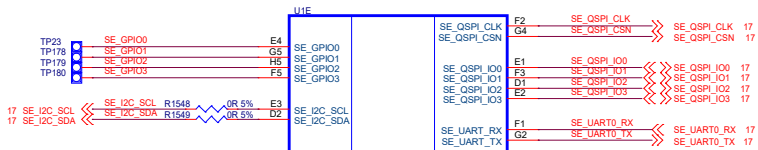
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# HDMI/DP/EDP/HDA/IS



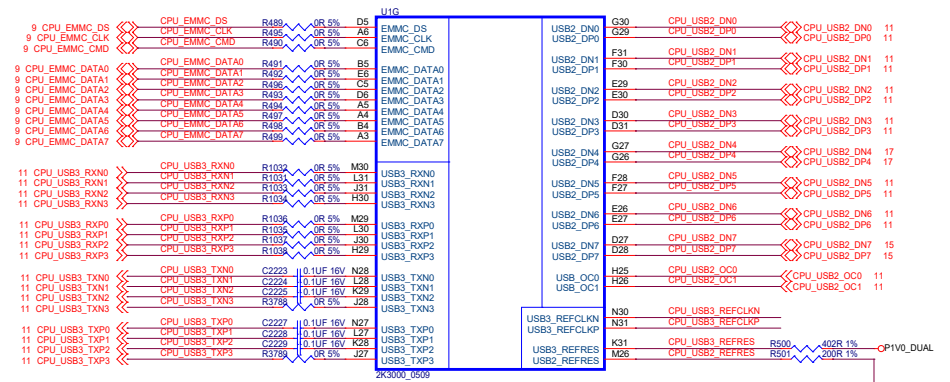
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# SE



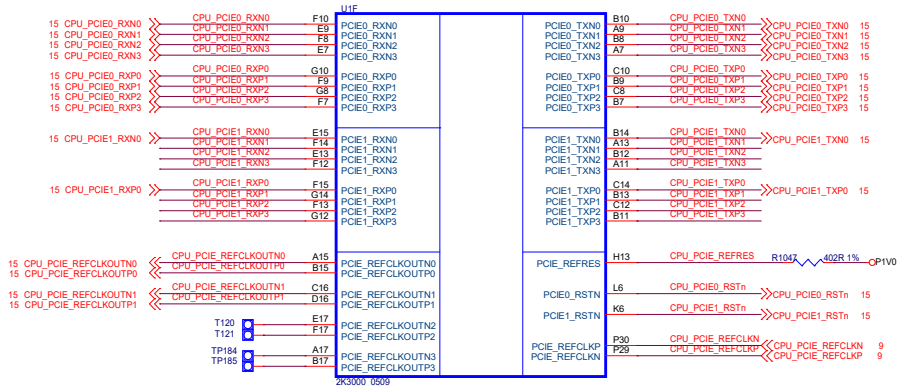
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# EMMC/USB3.0/USB2.0

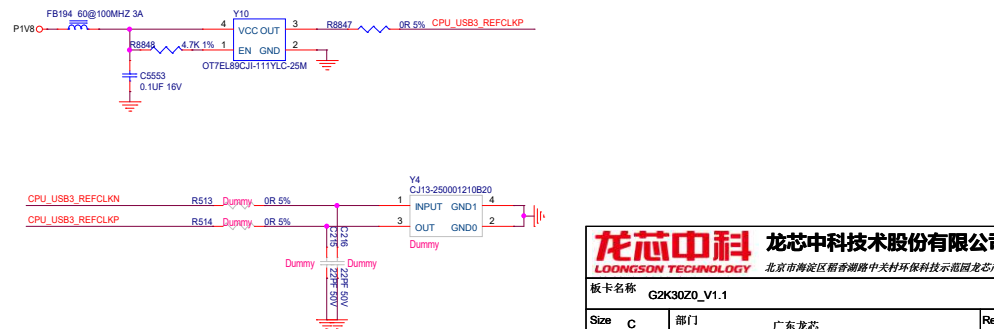


2K3000\_0509

# PCIe0/1



2K3000\_0509



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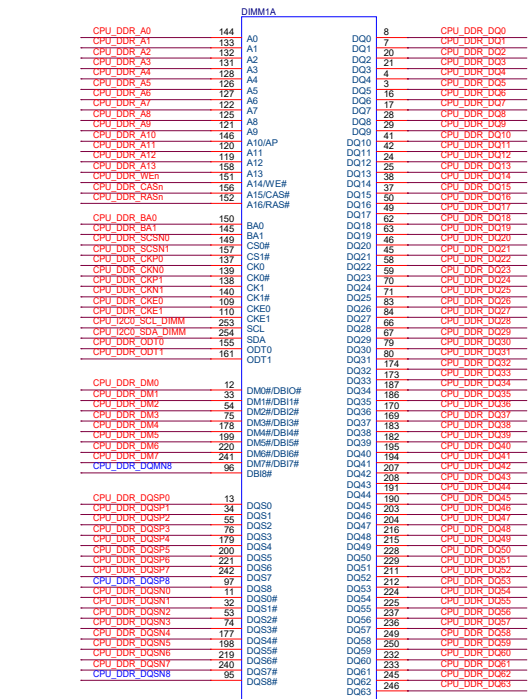
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Date: Wednesday, June 11, 2025 Sheet 6 of 24

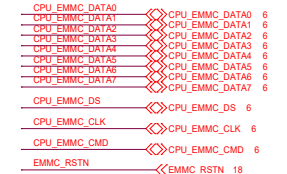
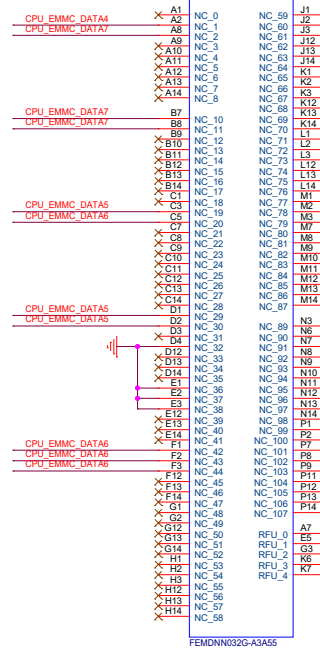
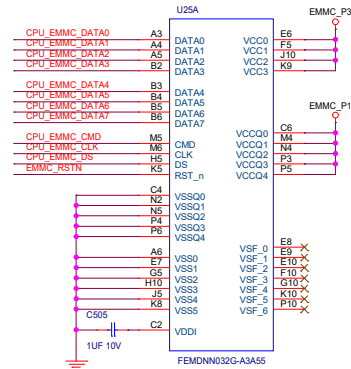
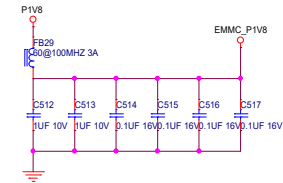
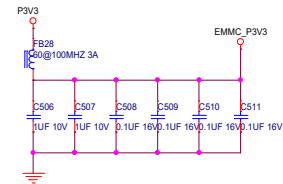
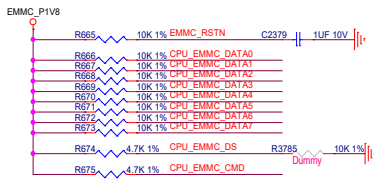
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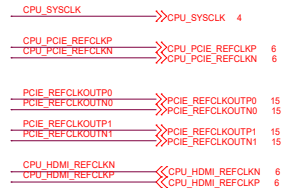
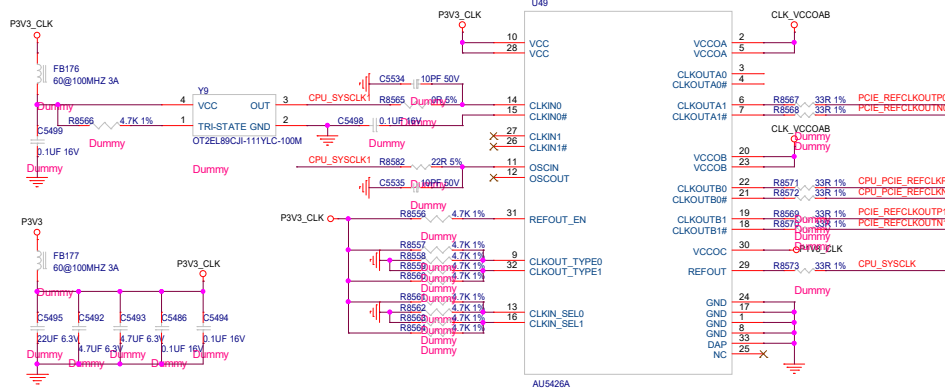
# DDR4 SODIMM



# EMMC



# CLOCK

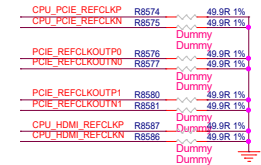
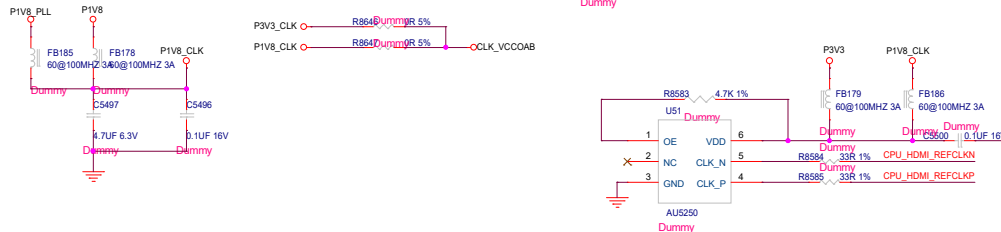


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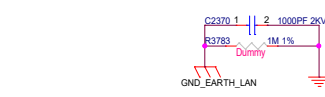
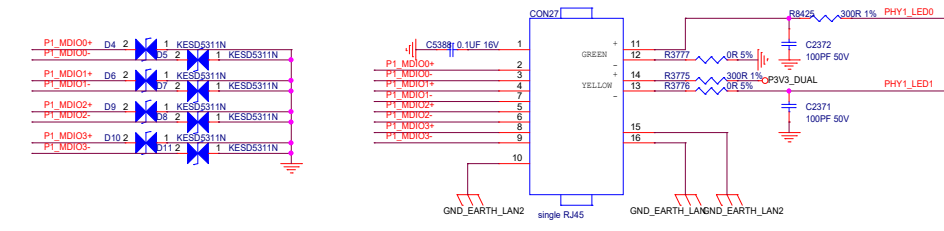
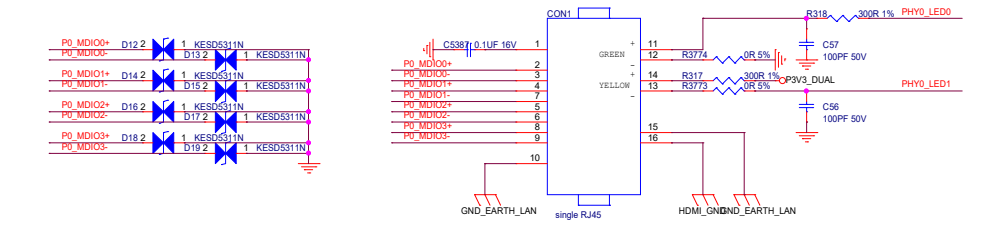
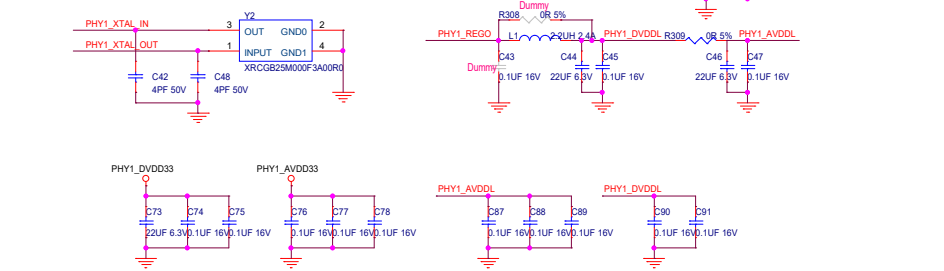
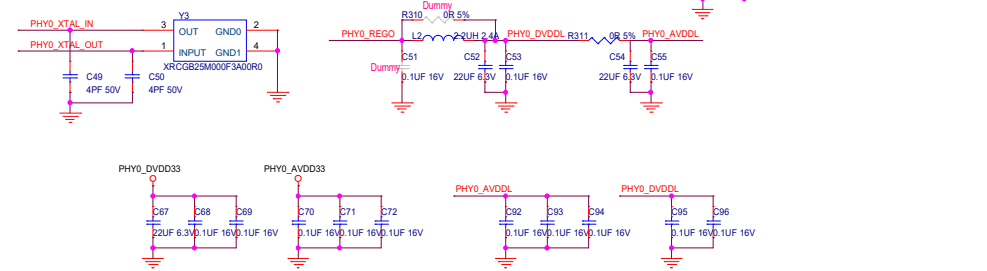
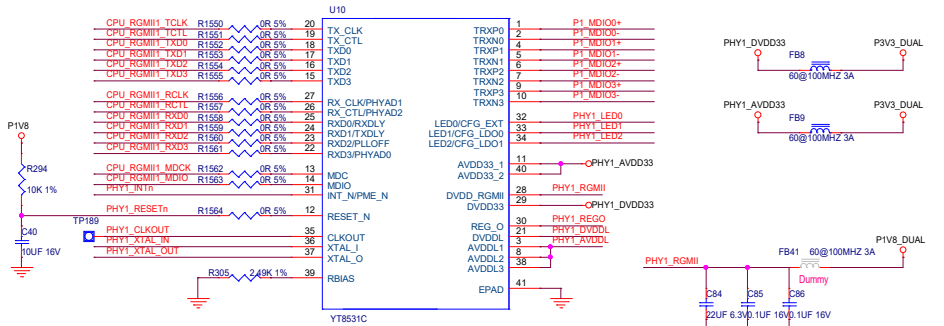
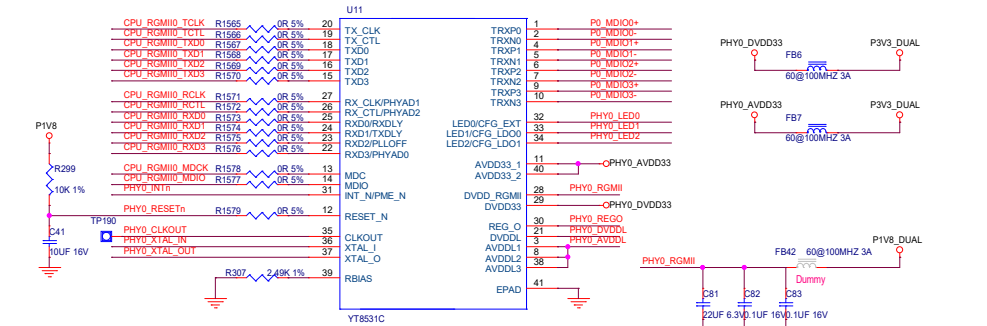
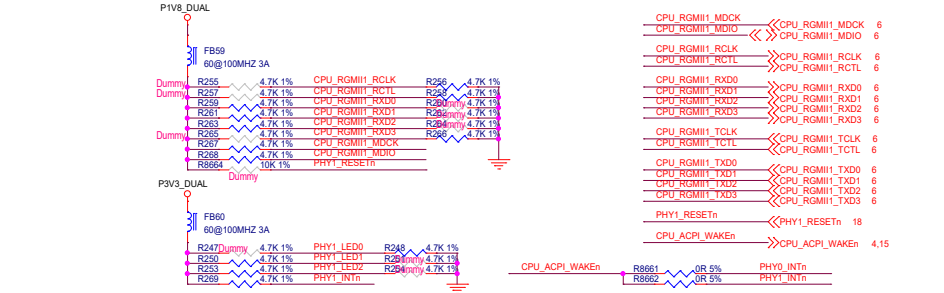
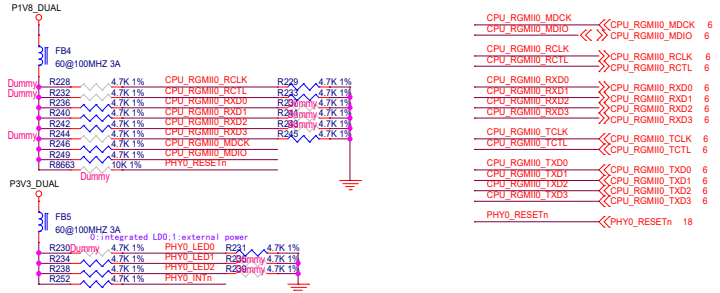
Clock_tpye1	Clock_tpye0	CLK Buffer Type
0	0	LVPECL
0	1	LVDS
1	0	HCSL
1	1	HIZ

## Input Clock Selection

CLKIn_SEL1	CLKIn_SEL0	Selected Clock
0	0	CLKIN0, CLKIN0*
0	1	CLKIN1, CLKIN1*
1	0	Crystal Or Crystal bypass AC coupled mode
1	1	Crystal bypass DC coupled mode



# LAN0/1



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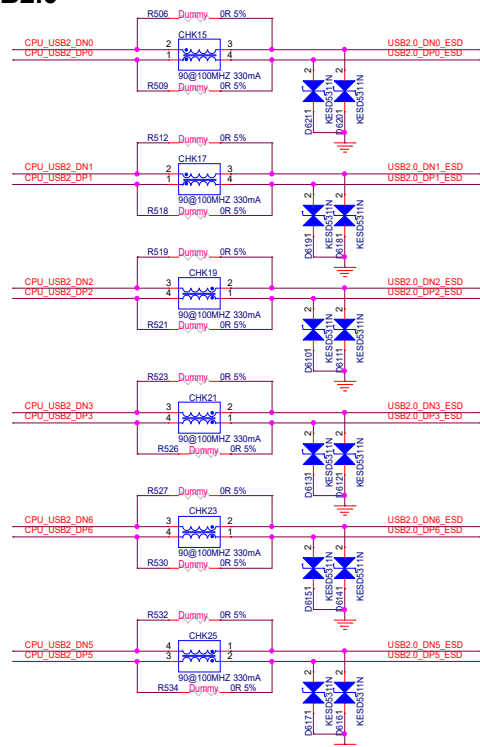
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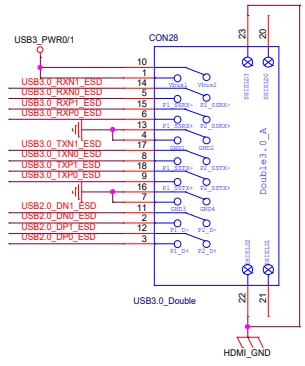
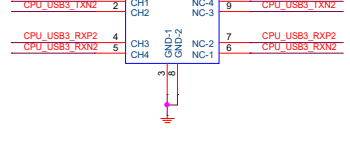
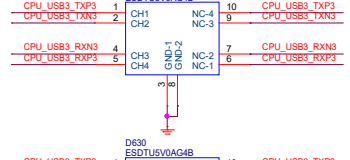
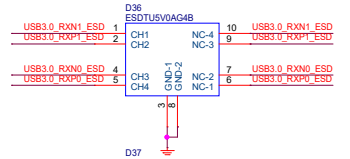
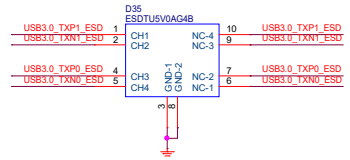
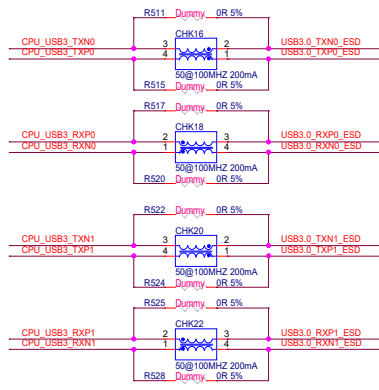
Date: Wednesday, June 11, 2025 Sheet 10 of 24

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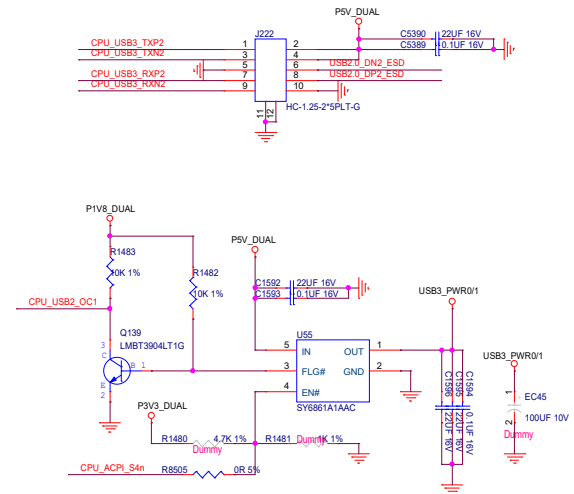
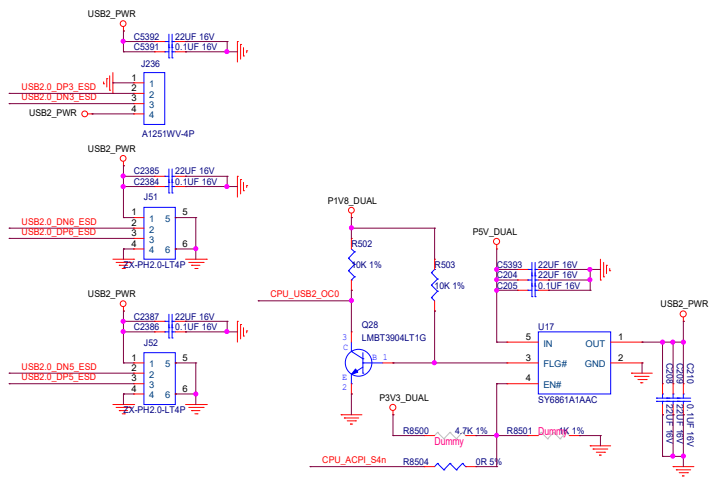
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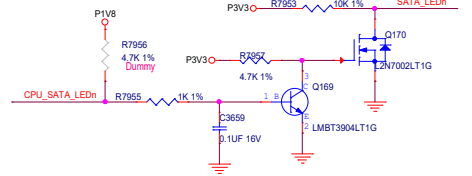
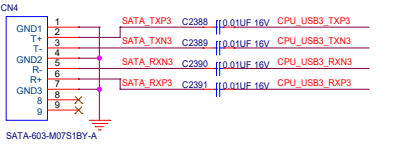
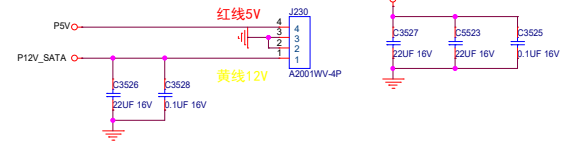
# USB3.0



- CPU\_USB2\_DN0 <<> CPU\_USB2\_DN0 6
- CPU\_USB2\_DP0 <<> CPU\_USB2\_DP0 6
- CPU\_USB2\_DN1 <<> CPU\_USB2\_DN1 6
- CPU\_USB2\_DP1 <<> CPU\_USB2\_DP1 6
- CPU\_USB2\_DN2 <<> CPU\_USB2\_DN2 6
- CPU\_USB2\_DP2 <<> CPU\_USB2\_DP2 6
- CPU\_USB2\_DN3 <<> CPU\_USB2\_DN3 6
- CPU\_USB2\_DP3 <<> CPU\_USB2\_DP3 6
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- CPU\_USB2\_DN6 <<> CPU\_USB2\_DN6 6
- CPU\_USB2\_DP6 <<> CPU\_USB2\_DP6 6
- CPU\_USB2\_OC0 <<> CPU\_USB2\_OC0 6
- CPU\_USB2\_OC1 <<> CPU\_USB2\_OC1 6
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- CPU\_USB3\_RXN1 <<> CPU\_USB3\_RXN1 6
- CPU\_USB3\_RXN2 <<> CPU\_USB3\_RXN2 6
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- CPU\_USB3\_RXP2 <<> CPU\_USB3\_RXP2 6
- CPU\_USB3\_RXP3 <<> CPU\_USB3\_RXP3 6
- CPU\_SATA\_LEDn <<> CPU\_SATA\_LEDn 4
- SATA\_LEDn <<> SATA\_LEDn 18
- CPU ACPI\_S4n <<> CPU ACPI\_S4n 4,15,21



# SATA



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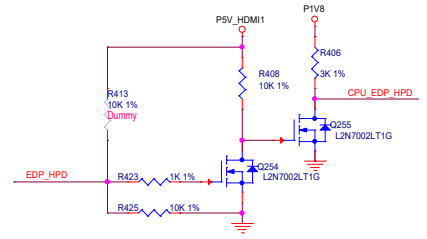
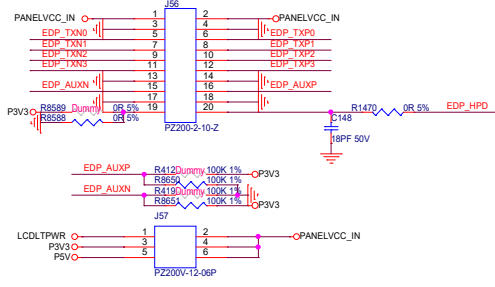
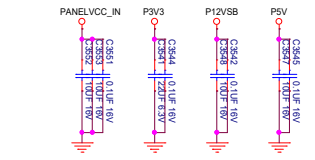
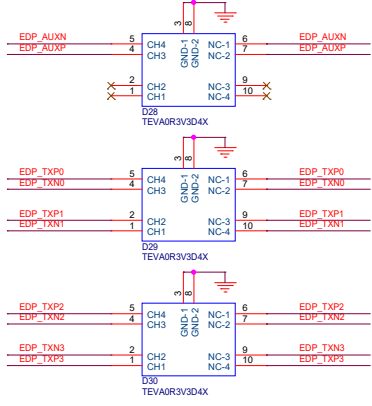
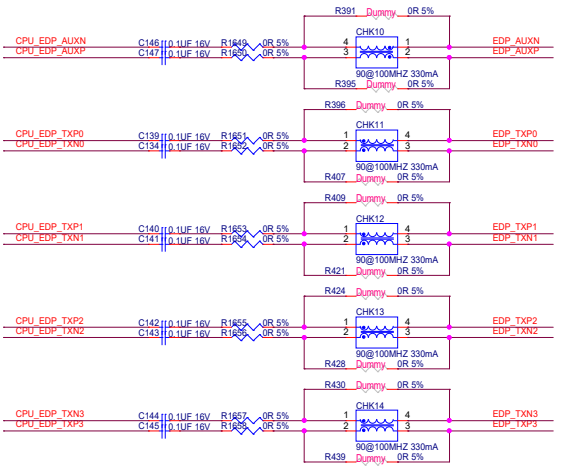
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Size C 部门 广东龙芯 Rev 1.1

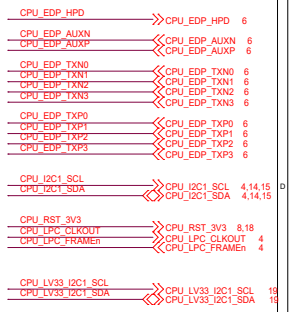
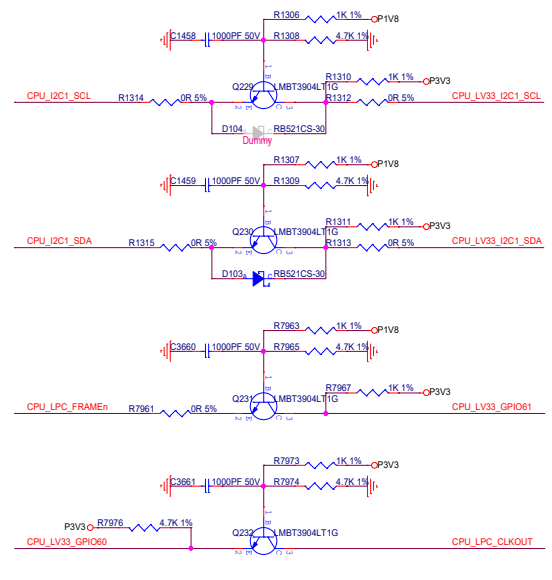
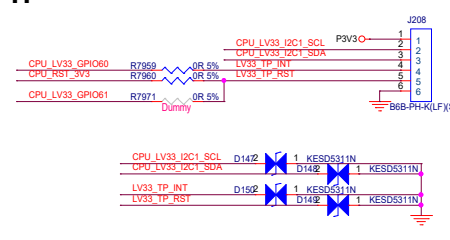
Date: Wednesday, June 11, 2025 Sheet 11 of 24

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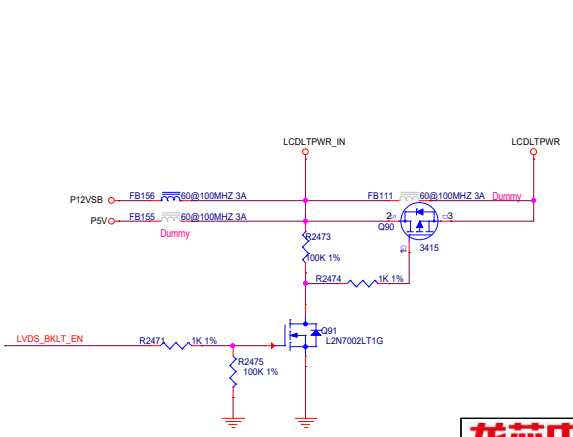
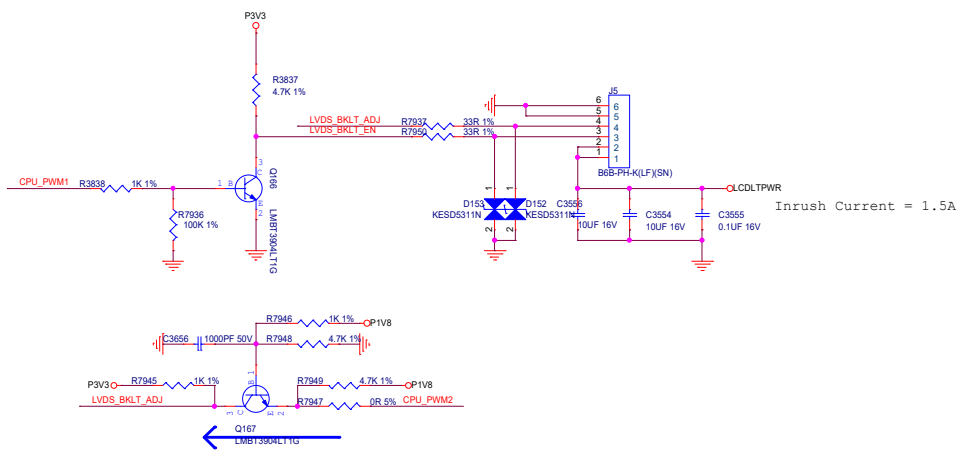
# eDP



# TP



# BACKLIGHT CONTROL



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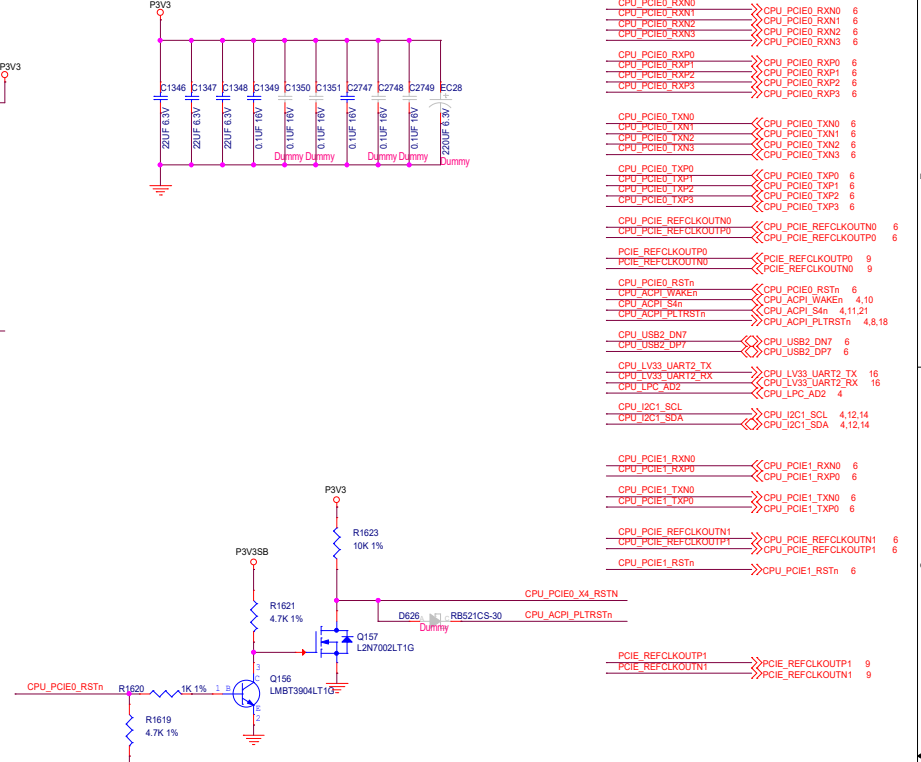
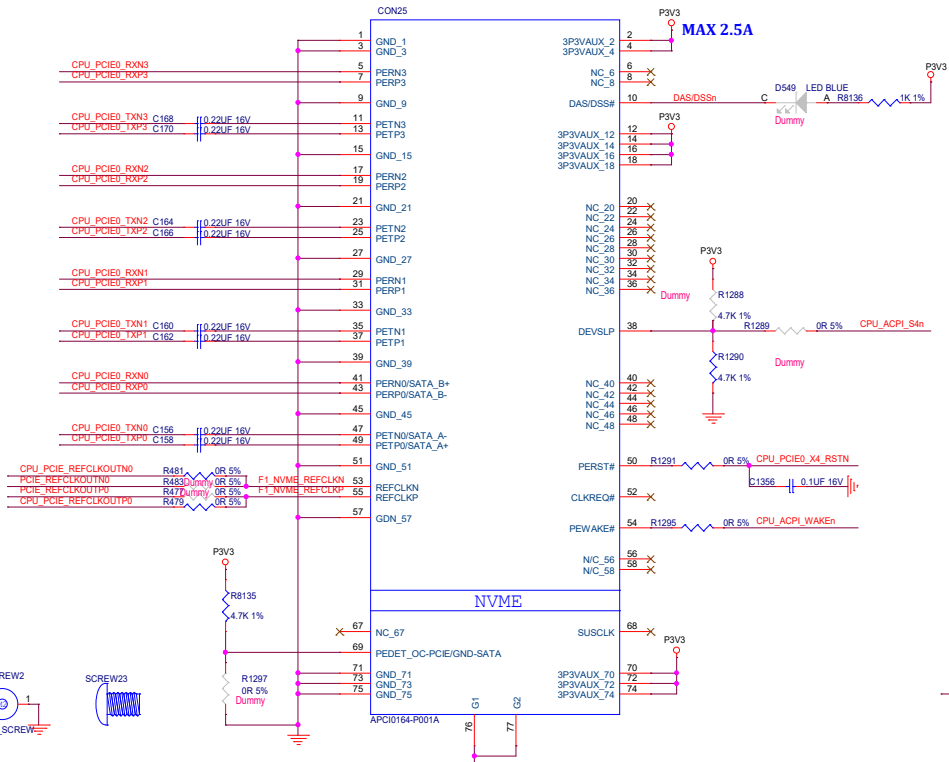
板卡名称 G2K30Z0\_V1.1

Size	C	部门	广东龙芯	Rev	1.1
Date	Wednesday, June 11, 2025			Sheet	12 of 24
Drawn By	<OrgAdmin>			Checked By	





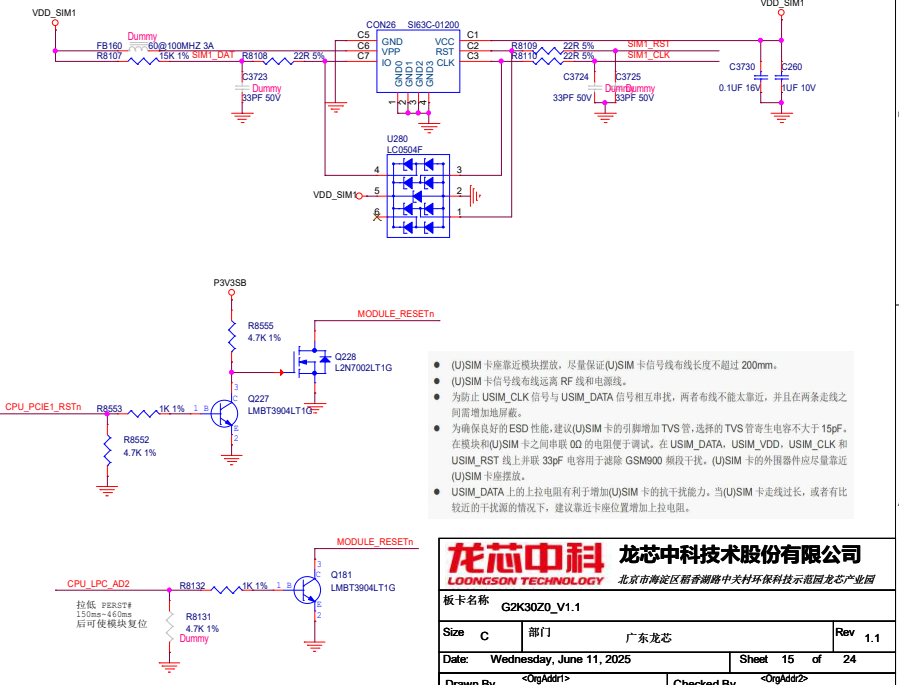
# M.2 NVME



CPU_PCIE_RXN0	CPU_PCIE_RXN0	6
CPU_PCIE_RXN1	CPU_PCIE_RXN1	6
CPU_PCIE_RXN2	CPU_PCIE_RXN2	6
CPU_PCIE_RXN3	CPU_PCIE_RXN3	6
CPU_PCIE_RXP0	CPU_PCIE_RXP0	6
CPU_PCIE_RXP1	CPU_PCIE_RXP1	6
CPU_PCIE_RXP2	CPU_PCIE_RXP2	6
CPU_PCIE_RXP3	CPU_PCIE_RXP3	6
CPU_PCIE_TXN0	CPU_PCIE_TXN0	6
CPU_PCIE_TXN1	CPU_PCIE_TXN1	6
CPU_PCIE_TXN2	CPU_PCIE_TXN2	6
CPU_PCIE_TXN3	CPU_PCIE_TXN3	6
CPU_PCIE_TXP0	CPU_PCIE_TXP0	6
CPU_PCIE_TXP1	CPU_PCIE_TXP1	6
CPU_PCIE_TXP2	CPU_PCIE_TXP2	6
CPU_PCIE_TXP3	CPU_PCIE_TXP3	6
CPU_PCIE_REFCLKOUT0	CPU_PCIE_REFCLKOUT0	6
CPU_PCIE_REFCLKOUT10	CPU_PCIE_REFCLKOUT10	6
PCIE_REFCLKOUT0	PCIE_REFCLKOUT0	9
PCIE_REFCLKOUT10	PCIE_REFCLKOUT10	9
CPU_PCIE_RSTn	CPU_PCIE_RSTn	6
CPU ACPI_WAKEn	CPU ACPI_WAKEn	4,10
CPU ACPI_S4n	CPU ACPI_S4n	4,11,21
CPU ACPI_PLTRStn	CPU ACPI_PLTRStn	4,8,18
CPU_USB2_DN7	CPU_USB2_DN7	6
CPU_USB2_DP7	CPU_USB2_DP7	6
CPU_LV33_UART2_TX	CPU_LV33_UART2_TX	16
CPU_LV33_UART2_RX	CPU_LV33_UART2_RX	16
CPU_LPC_AD2	CPU_LPC_AD2	4
CPU_I2C1_SCL	CPU_I2C1_SCL	4,12,14
CPU_I2C1_SDA	CPU_I2C1_SDA	4,12,14

CPU_PCIE1_RXN0	CPU_PCIE1_RXN0	6
CPU_PCIE1_RXP0	CPU_PCIE1_RXP0	6
CPU_PCIE1_TXN0	CPU_PCIE1_TXN0	6
CPU_PCIE1_TXP0	CPU_PCIE1_TXP0	6
PCIE_REFCLKOUTP1	PCIE_REFCLKOUTP1	9
PCIE_REFCLKOUTN1	PCIE_REFCLKOUTN1	9
CPU_PCIE1_RSTn	CPU_PCIE1_RSTn	6

# MINI PCIE



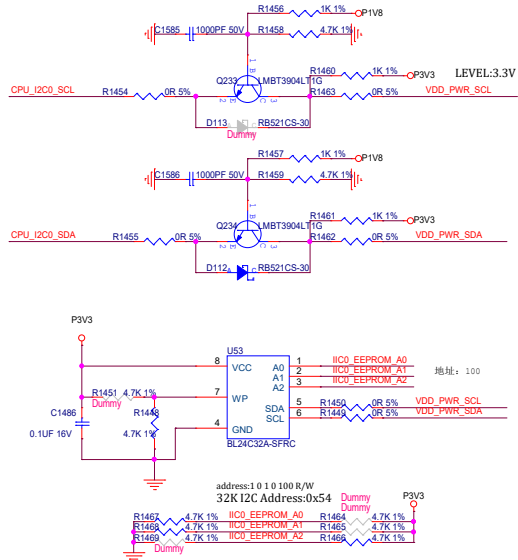
- (U)SIM卡座靠近模块摆放, 尽量保证(U)SIM卡信号线布线长度不超过200mm.
- (U)SIM卡信号线布线远离RF天线和电源线.
- 为防止USIM\_CLK信号与USIM\_DATA信号相互串扰, 两者布线不能太靠近, 并且在两条布线之间增加电阻.
- 为确保良好的ESD性能, 建议(U)SIM卡的引脚增加TVS管, 选择的TVS管寄生电容不大于15pF. 在模块和(U)SIM卡之间串联0Ω的电阻便于测试. 在USIM\_DATA, USIM\_VDD, USIM\_CLK和USIM\_RST线上串联33pF电容用于滤除GSM/900频段干扰. (U)SIM卡的外围器件应尽量靠近(U)SIM卡座摆放.
- USIM\_DATA上的上拉电阻有利于增加(U)SIM卡的抗干扰能力. 当(U)SIM卡走线过长, 或者有比较近的干扰源的情况下, 建议靠近卡座位置增加上拉电阻.

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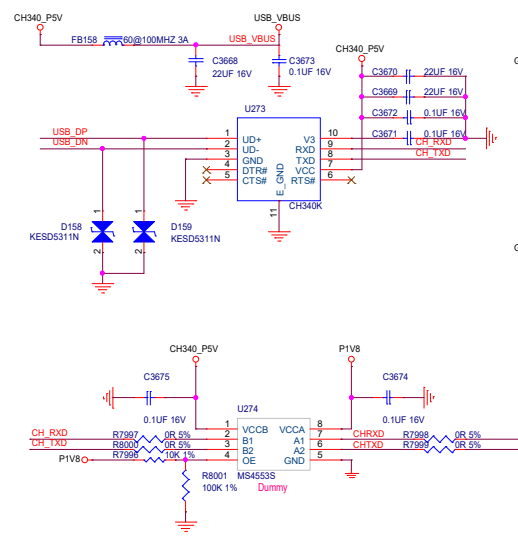
板卡名称: G2K30Z0\_V1.1

Size	C	部门	广东龙芯	Rev	1.1
Date:	Wednesday, June 11, 2025			Sheet	15 of 24
Drawn By	<OrgAdd>			Checked By	<OrgAdd>

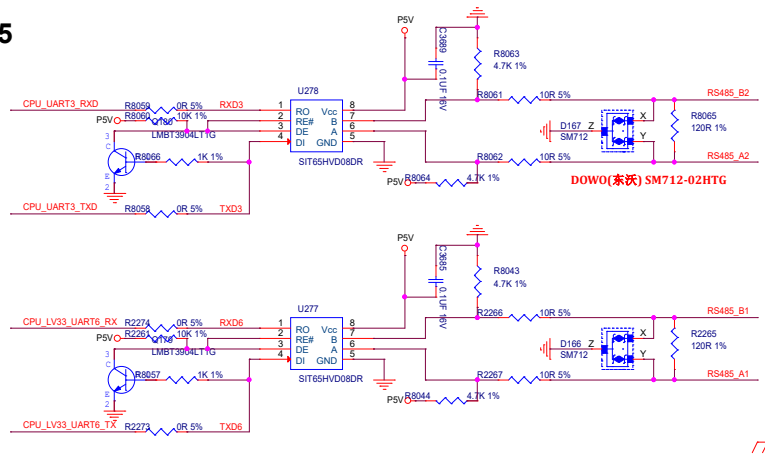
# IIC



# DEBUG UART0

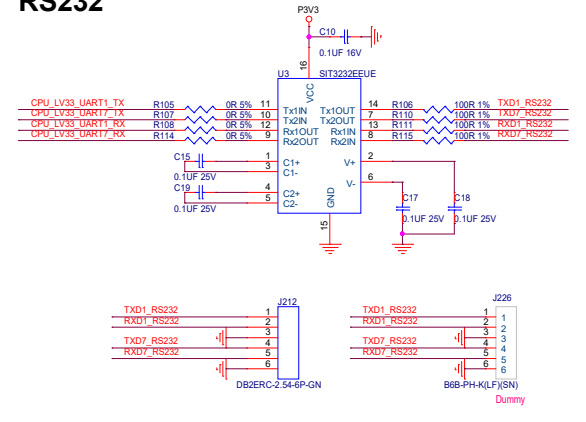


# RS485

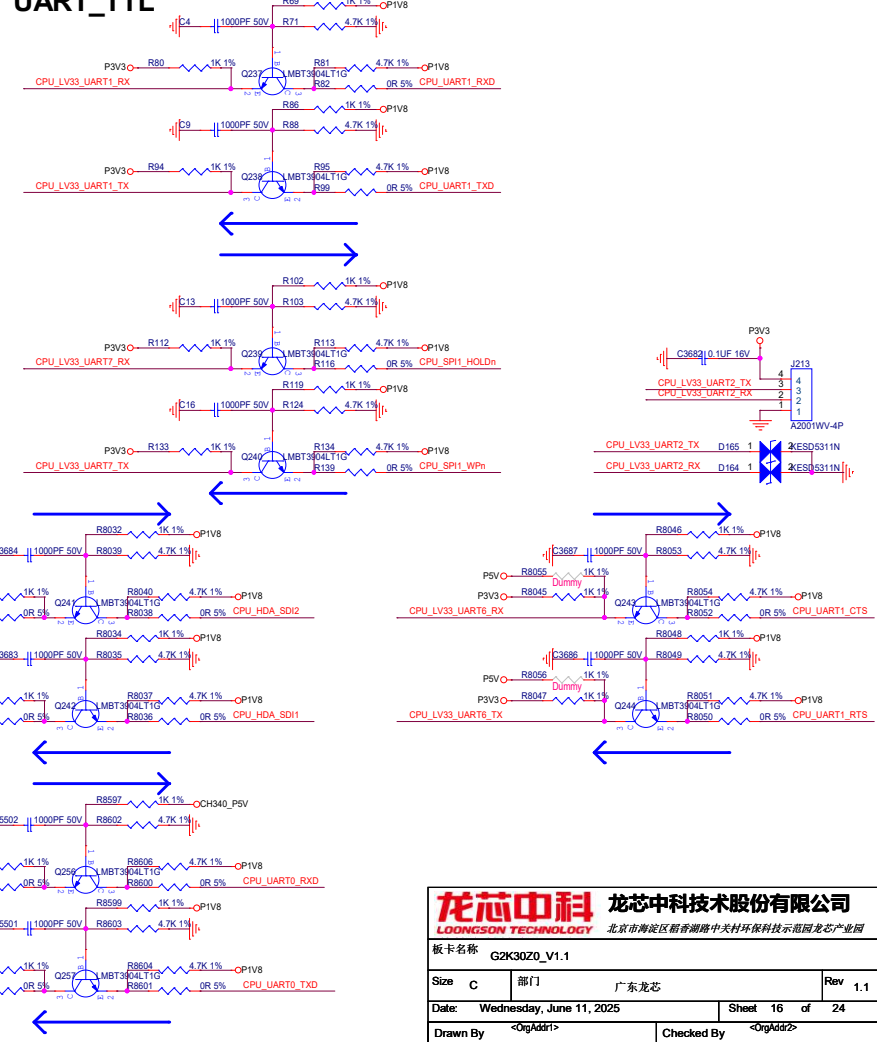


- CPU\_I2C0\_SCL >>> CPU\_I2C0\_SCL 4
- CPU\_I2C0\_SDA >>> CPU\_I2C0\_SDA 4
- VDD\_PWR\_SCL >>> VDD\_PWR\_SCL 23
- VDD\_PWR\_SDA >>> VDD\_PWR\_SDA 23
- CPU\_I2C0\_SCL\_DIMM >>> CPU\_I2C0\_SCL\_DIMM 8
- CPU\_I2C0\_SDA\_DIMM >>> CPU\_I2C0\_SDA\_DIMM 8
- CPU\_UART0\_RXD >>> CPU\_UART0\_RXD 4
- CPU\_UART0\_TXD >>> CPU\_UART0\_TXD 4
- CPU\_UART1\_RXD >>> CPU\_UART1\_RXD 4
- CPU\_UART1\_TXD >>> CPU\_UART1\_TXD 4
- CPU\_HDA\_SDI2 >>> CPU\_HDA\_SDI2 6,14
- CPU\_HDA\_SDI1 >>> CPU\_HDA\_SDI1 6,14
- CPU\_UART3\_TXD >>> CPU\_UART3\_TXD 17
- CPU\_UART3\_RXD >>> CPU\_UART3\_RXD 17
- CPU\_UART1\_CTS >>> CPU\_UART1\_CTS 4
- CPU\_UART1\_RTS >>> CPU\_UART1\_RTS 4
- CPU\_UART1\_HOLDn >>> CPU\_UART1\_HOLDn 4
- CPU\_SPH1\_WPm >>> CPU\_SPH1\_HOLDn 4
- CPU\_SPH1\_HOLDn >>> CPU\_SPH1\_HOLDn 4
- CPU\_SPH1\_WPm >>> CPU\_SPH1\_HOLDn 4
- CPU\_LV33\_UART2\_TX >>> CPU\_LV33\_UART2\_TX 15
- CPU\_LV33\_UART2\_RX >>> CPU\_LV33\_UART2\_RX 15

# RS232



# UART\_TTL



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板卡名称 G2K3020\_V1.1

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Date: Wednesday, June 11, 2025 Sheet 16 of 24

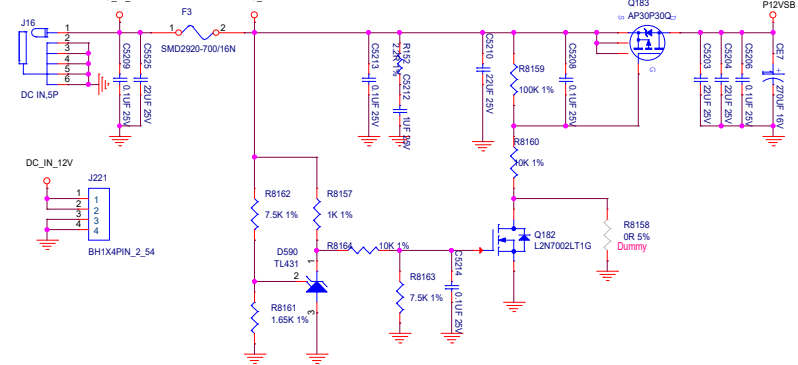
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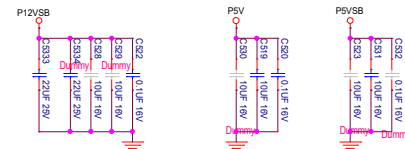


# DC\_IN

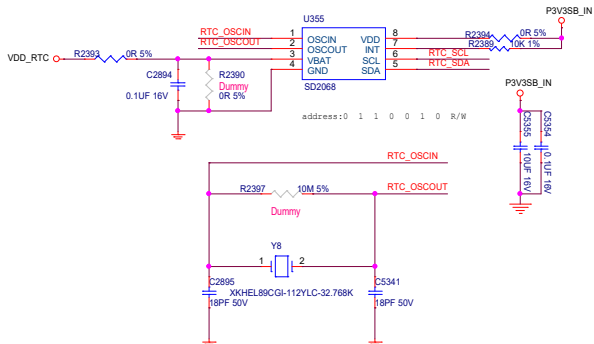
12V DC IN



- CPU\_VDD\_SOC\_P0V8\_PG <<> CPU\_VDD\_SOC\_P0V8\_PG 23
- BOARD\_PWR\_PG <<> BOARD\_PWR\_PG 4,18
- P1V2\_DUAL\_PG <<> P1V2\_DUAL\_PG 21
- P0V8\_DUAL\_PG <<> P0V8\_DUAL\_PG 21
- CPU\_ACPI\_S3h <<> CPU\_ACPI\_S3h 4,8,18,21,22
- CPU\_ACPI\_RSMRStn <<> CPU\_ACPI\_RSMRStn 4,18
- CPU\_ACPI\_PWRBTn <<> CPU\_ACPI\_PWRBTn 4,18
- CPU\_LV33\_I2C1\_SCL <<> CPU\_LV33\_I2C1\_SCL 12
- CPU\_LV33\_I2C1\_SDA <<> CPU\_LV33\_I2C1\_SDA 12
- VDD\_GPU\_PG <<> VDD\_GPU\_PG 23
- VDD\_NODE\_PG <<> VDD\_NODE\_PG 23

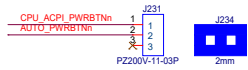
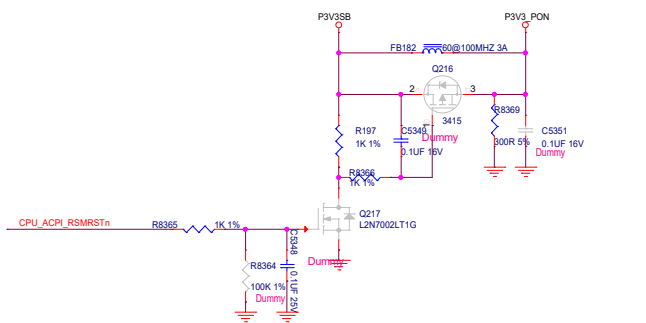
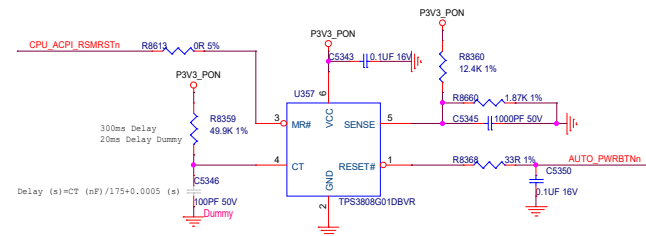


# E\_RTC



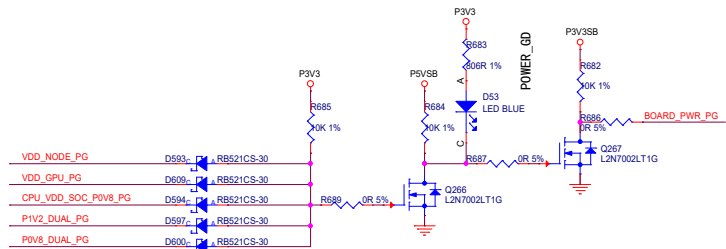
- CPU\_LV33\_I2C1\_SCL <<> CPU\_LV33\_I2C1\_SCL 100R, 1% RTC\_SCL
- CPU\_LV33\_I2C1\_SDA <<> CPU\_LV33\_I2C1\_SDA 100R, 1% RTC\_SDA

# PWR\_ON

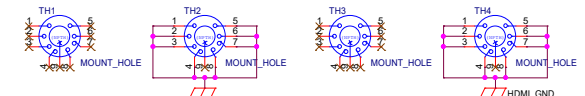


POWER ON	
1-2	AUTO
2-3	NORMAL

# PG



- VDD\_NODE\_PG <<> D593, RB521CS-30
- VDD\_GPU\_PG <<> D609, RB521CS-30
- CPU\_VDD\_SOC\_P0V8\_PG <<> D594, RB521CS-30
- P1V2\_DUAL\_PG <<> D597, RB521CS-30
- P0V8\_DUAL\_PG <<> D600, RB521CS-30



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板卡名称 G2K30Z0\_V1.1

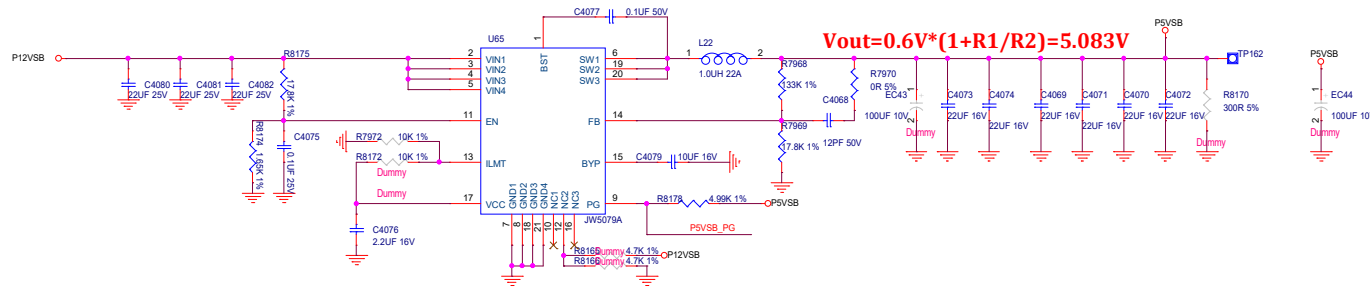
Size C 部门 广东龙芯 Rev 1.1

Date: Wednesday, June 11, 2025 Sheet 19 of 24

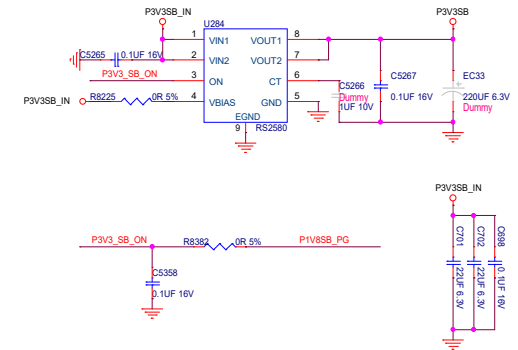
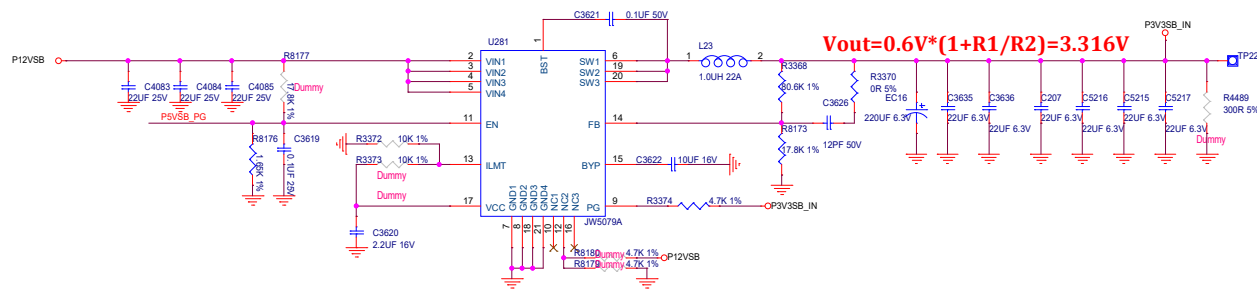
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# P5V5B

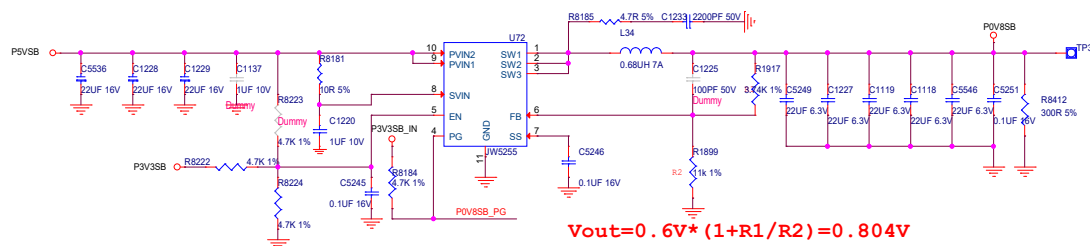
P0V8SB\_PG << P0V8SB\_PG 21  
P1V8SB\_EN << P1V8SB\_EN 18



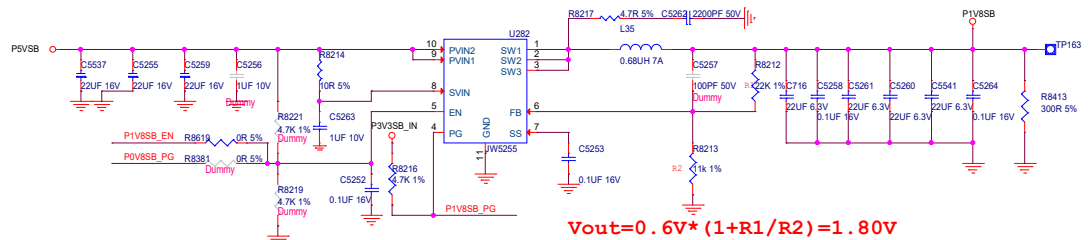
# P3V3SB



# P0V8SB



# P1V8SB

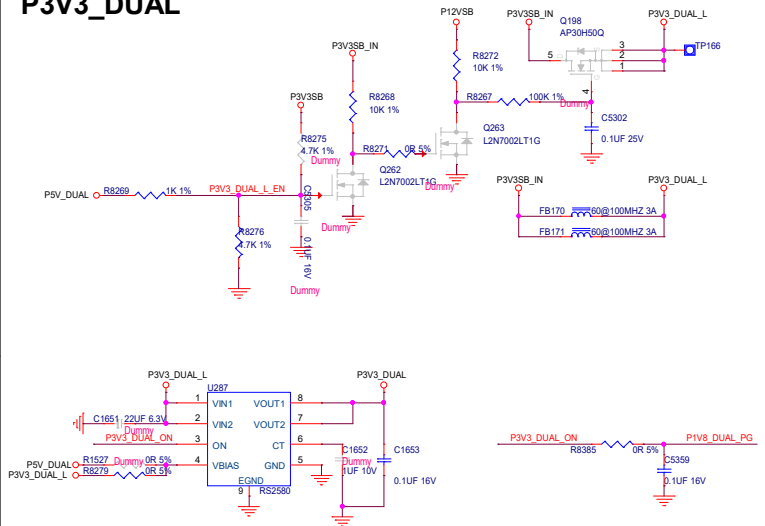


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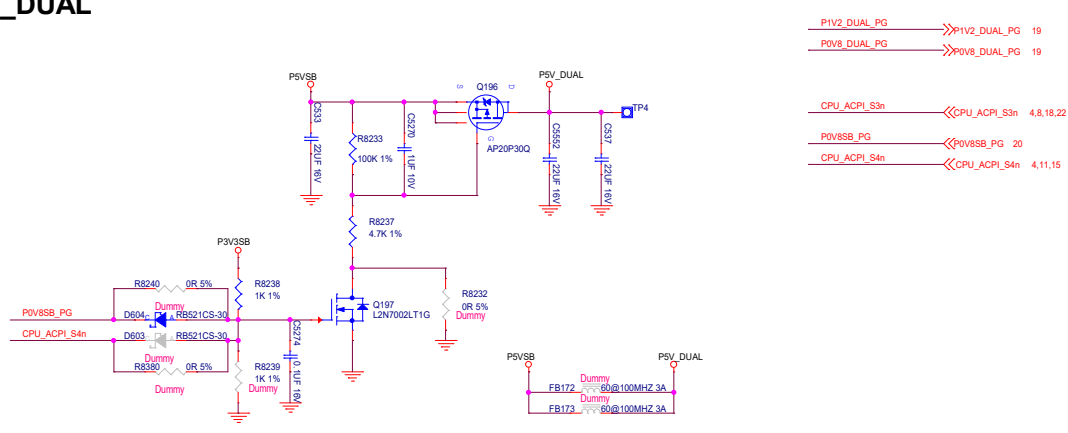
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Size	C	部门	广东龙芯	Rev	1.1
Date:	Wednesday, June 11, 2025			Sheet	20 of 24
Drawn By	<OrgAdd1>	Checked By	<OrgAdd2>		

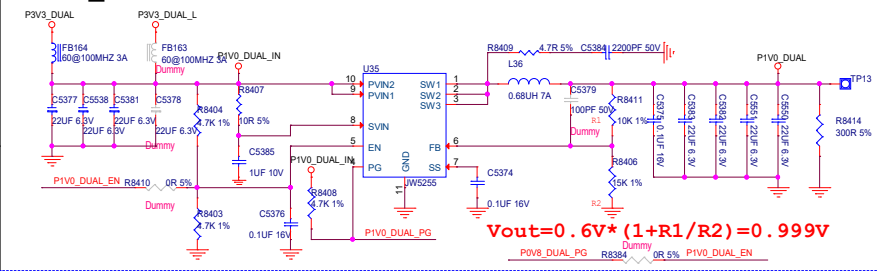
### P3V3\_DUAL



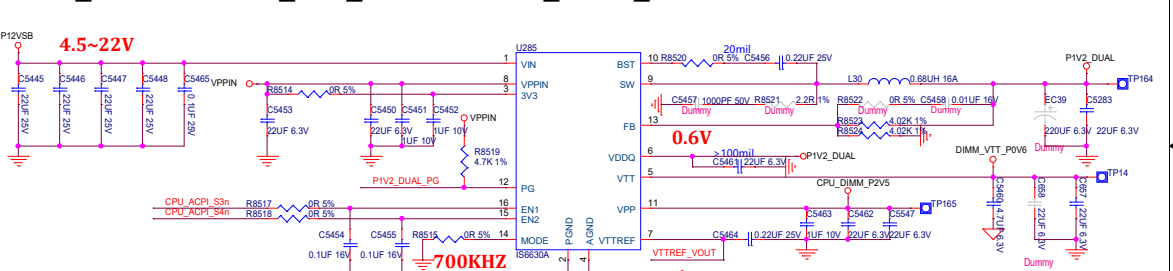
### P5V\_DUAL



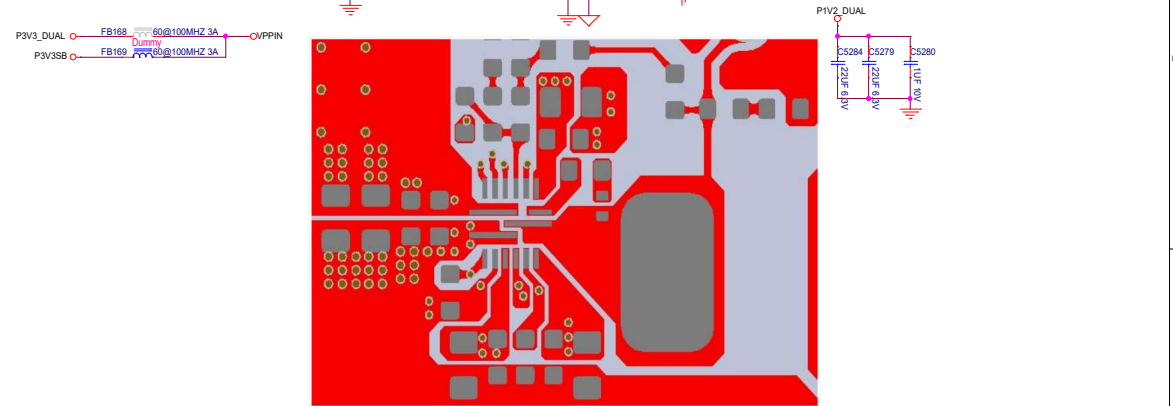
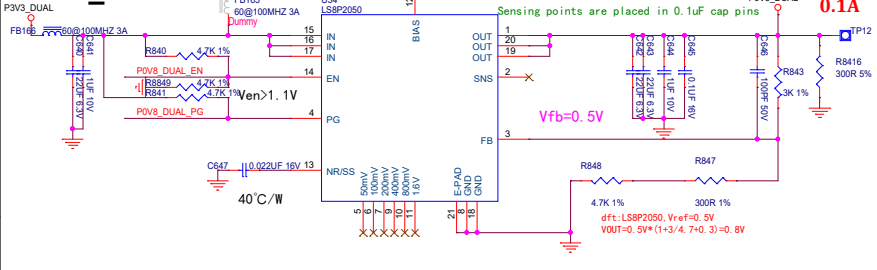
### P1V0\_DUAL



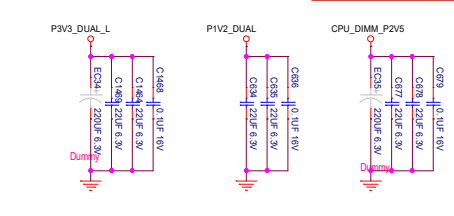
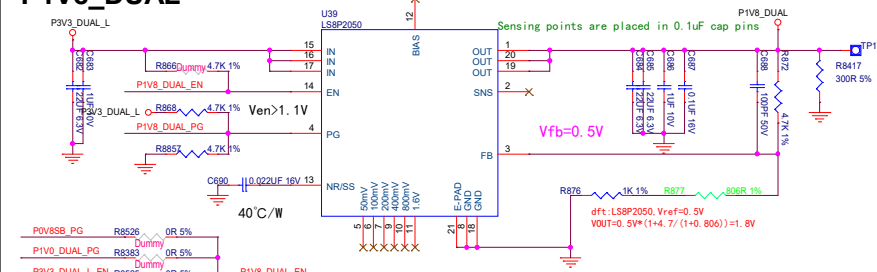
### P1V2\_DUAL DIMM\_VTT\_P0V6 CPU\_DIMM\_P2V5



### P0V8\_DUAL



### P1V8\_DUAL



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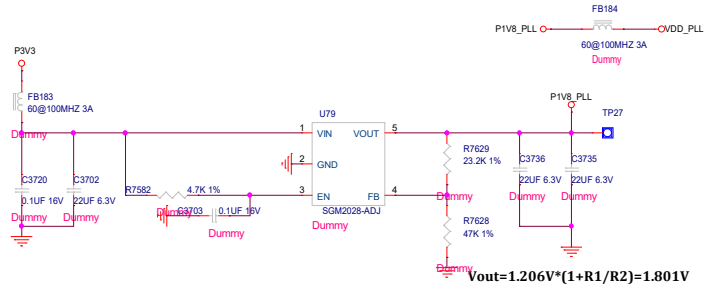
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Size: C 部门: 广东龙芯 Rev: 1.1

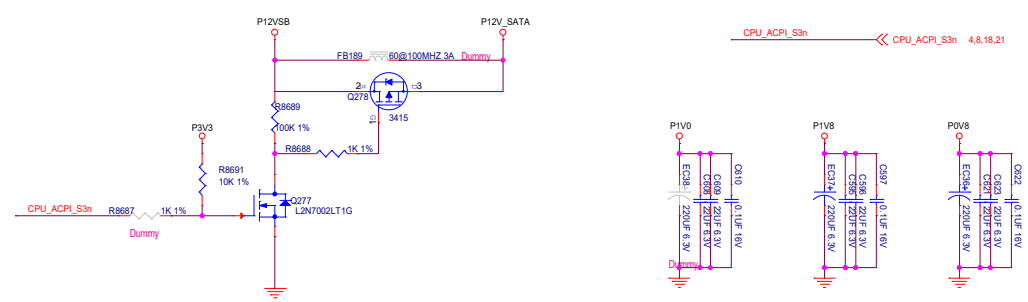
Date: Wednesday, June 11, 2025 Sheet 21 of 24

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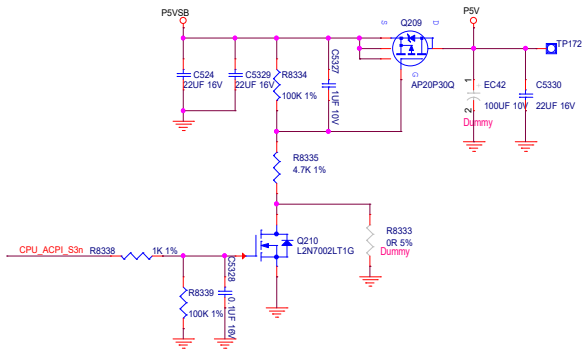
### P1V8\_PLL



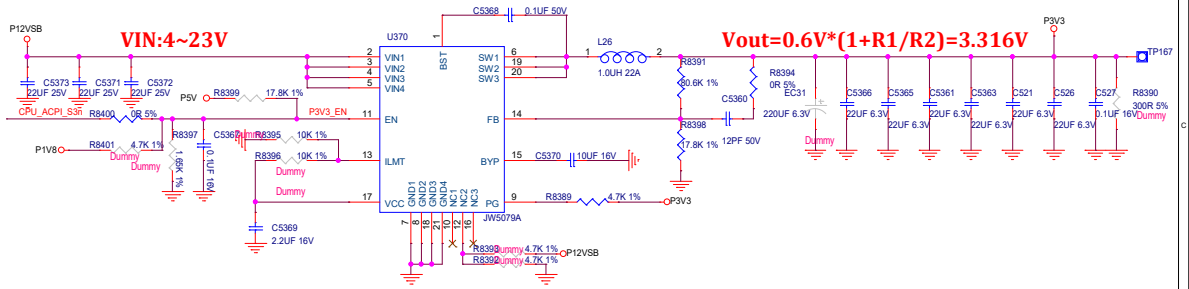
### P12V\_SATA



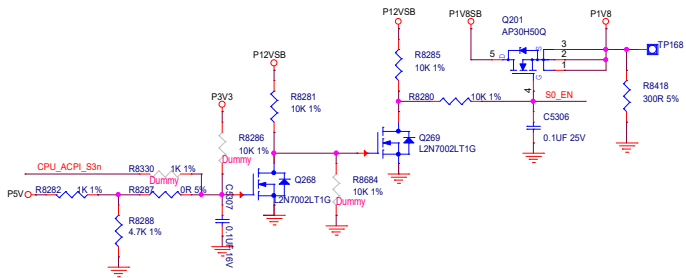
### P5V



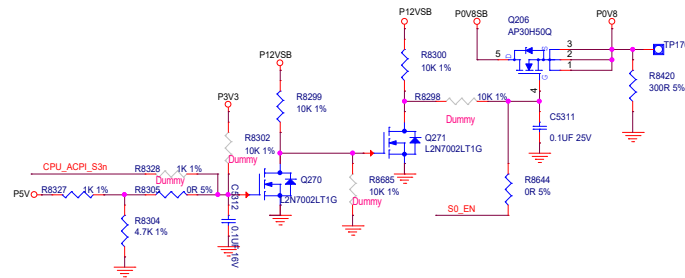
### P3V3



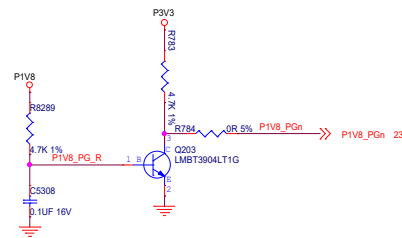
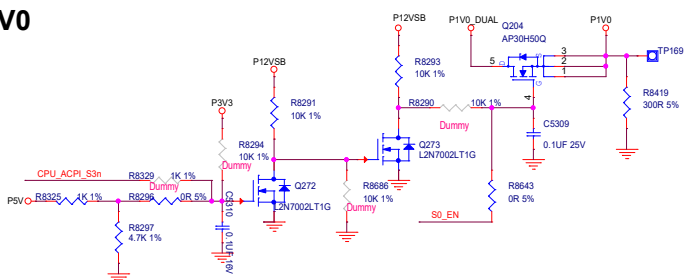
### P1V8



### P0V8

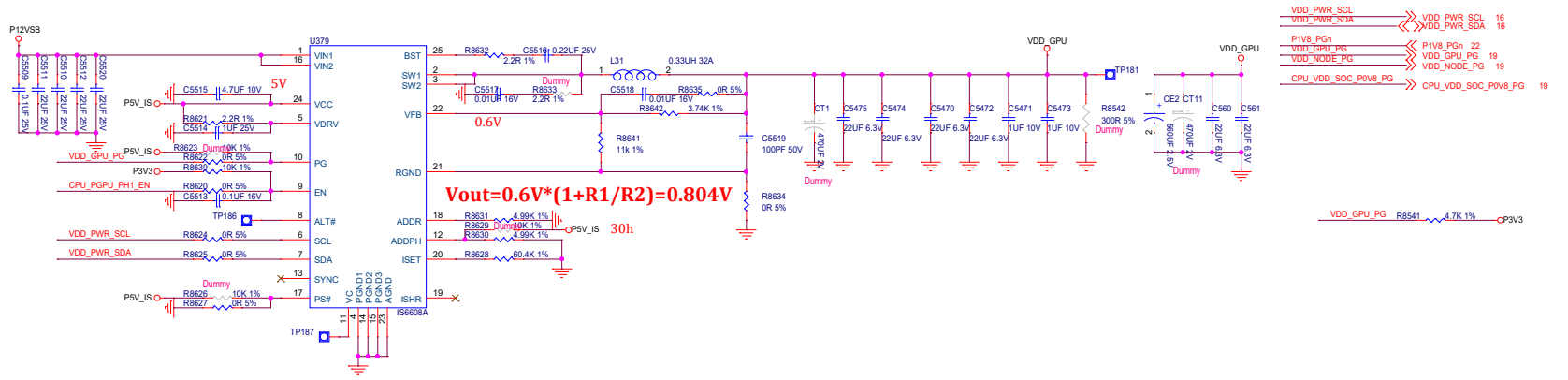


### P1V0



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板卡名称 G2K30Z0_V1.1			
Size C	部门	广东龙芯	Rev 1.1
Date: Wednesday, June 11, 2025		Sheet 22 of 24	
Drawn By <OrgAdd1>		Checked By <OrgAdd2>	

# VDD\_GPU



# REVISION HISTORY

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板卡名称 G2K30Z0_V1.1	
Size C	部门 广东龙芯
Date: Wednesday, June 11, 2025	Sheet 24 of 24
Drawn By <OrgAddr1>	Checked By <OrgAddr2>